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File View Edit Tools Window Help

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  - L1: (156) ("low pin cou
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DBs USPAT

Default operator: OR

Plurals

Highlight all hit terms initially

BRS form | IS&R form | Image | Text | HTML

	Type	L #	Hits	Search Text	DBs	Time Stam	Comment	Error Definit	Er
1	BRS	L1	156	("low pin count" or LPC) near5 memory	USPAT	2005/09/2 2 16:58			
2	BRS	L2	8	l1 and (firmware near5 memory)	USPAT	2005/09/2 2 16:59			

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l1 and (firmware near5 memory)

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	U	1	Document ID	Issue Dat	Pages	Title	Current OR	Current XR
1	☐	☐	US 6927991 B2	20050809	9	Memory device accessible with differe	365/63	365/189.07 ;
2	☐	☐	US 6904484 B1	20050607	7	Low pin count (LPC) firmware hub recovery	710/300	365/185.08 ;
3	☐	☐	US 6851014 B2	20050201	13	Memory device having automatic protocol dete	711/103	711/145; 713/2
4	☐	☐	US 6421765 B1	20020716	9	Method and apparatus for selecting functiona	711/145	711/100; 711/154;
5	☐	☐	US 6381693 B2	20020430	10	Arrangements having firmware support for di	713/1	713/2
6	☐	☐	US 5835965 A	19981110	23	Memory system with multiplexed input-outpu	711/211	711/202
7	☐	☐	US 5829016 A	19981027	11	Memory system with multiplexed input-outpu	711/111	365/189.02 ;
8	☐	☐	US 5091950 A	19920225	31	Arabic language translating device with	704/277	704/3; 704/7

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